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## APPLICANTS

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yes  
 on

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/986,453 11/08/2001 PAT 6,713,688

yes on

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN P2000-397329 12/27/2000

yes on

IF REQUIRED, FOREIGN FILING LICENSE  
GRANTED

\*\* 01/21/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's signature: <i>[Signature]</i> Initials: <i>mn</i>	JAPAN	7	4	1

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## TITLE

Method of Manufacturing Circuit Board.

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees
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